



ATSCcheduler

PRELIMINARY DATASHEET

ATSC Gateway for ATSC3.0 broadcasting

ATSCHEUDLER IS ENENSYS' ATSC SCHEDULER MODULE THAT ENCAPSULATES IP OR ALP STREAMS INTO A ATSC MULTIPLEX, INSERTS SYNCHRONIZATION DATA FOR SINGLE FREQUENCY NETWORK BROADCASTING, ALLOCATES DATA INTO THE DIFFERENT PHYSICAL LAYER PIPES AND GENERATES STL PACKETS OVER IP.

Running at the station or central head-end, the **ATSCcheduler** encapsulates one or several IP Streams stemming from various HEVC encoders that delivers the compressed audiovisual content over ROUTE or MMTP protocols. It outputs the resulting ATSC compliant multiplex using the **STL** (Studio to Transmitter Link) protocol through IP. The **ATSCcheduler** runs in the HDc chassis that can embed up to 6x **ATSCcheduler** modules in 1U: the same chassis can output up to 6 STL streams over IP.

SCHEDULER WITH MULTIPLE PLP SUPPORT

The **ATSCcheduler** is the central body of the operational ATSC network as it provides in-band control and signaling to all the ATSC modulators. When using Multiple PLP (Physical Layer Pipes) feature to provide service-specific robustness, the **ATSCcheduler** enables all the modulators to generate the same PLP data in a deterministic manner. The **ATSCcheduler** supports by default the management of 4 PLP.

SFN MANAGEMENT

Also the **ATSCcheduler** enables SFN broadcasting over ATSC. It provides in-band synchronization information through STL interface to all modulators to generate the same data at the very same time over the same frequency.

ALP ENCAPSULATION

The **ATSCcheduler** performs ALP (ATSC Link Layer Protocol) encapsulation of any incoming IP streams prior the encapsulation into BaseBand Frames. It generates also the LMT tables (Link Mapping Tables) to link the PLP to IP streams.

APPLICATIONS

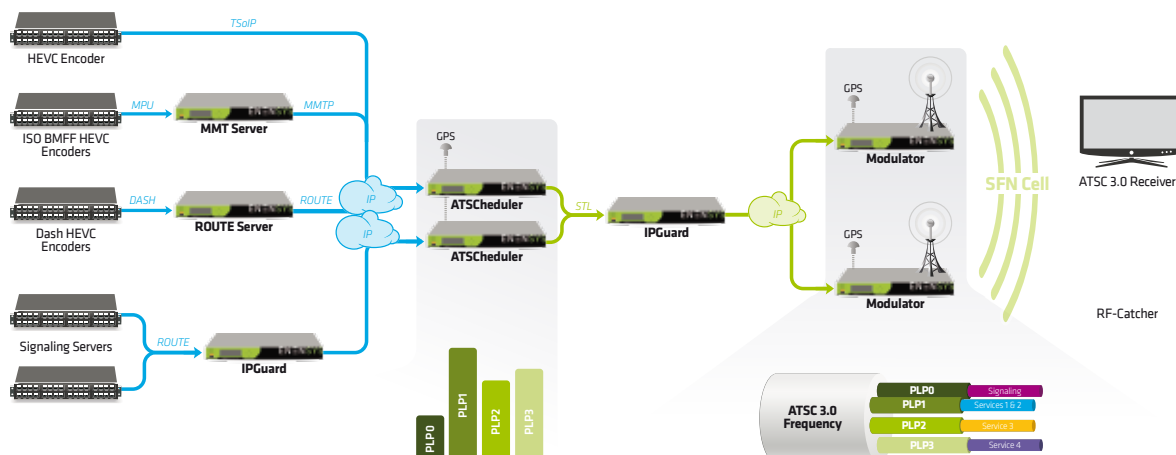
- ATSC SFN build-up
- ATSC Mobile broadcasting
- ATSC Multi-PLP management

BENEFITS

- Embedded in High Density chassis (HDc):
 - to allow multiple ATSC in 1U
 - to combine with IPGuard, ...
 - to enable future-proof technology
- Central body of the ATSC network
- Quick handling of the ATSC complexity
- Improve coverage when broadcasting over SFN

CHARACTERISTICS

- ROUTE, MMTP or ALP input
- Encapsulation of IP streams into ALP
- Encapsulation into ATSC baseband frame
- LMT tables generation
- Configuration of ATSC modulators
- ATSC SFN Adaption
- Single and Multiple PLP management
- Generation of STL packets over IP
- Validation of ATSC transmission parameters
- Full SNMPv2 support





PRELIMINARY DATASHEET

INPUTS

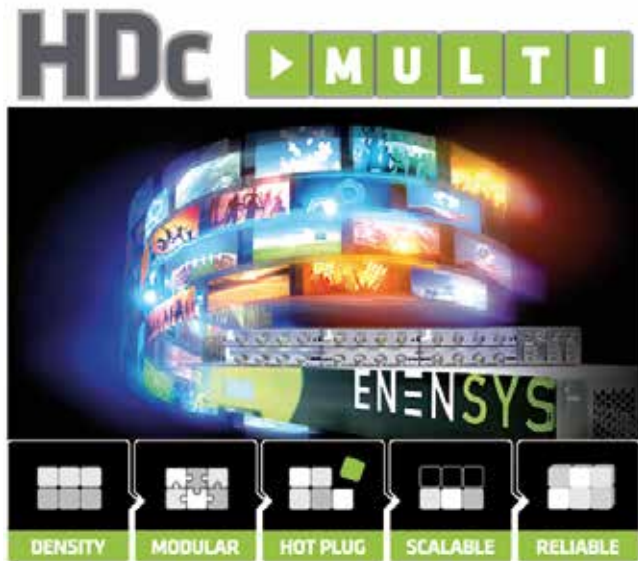
Control	1x Gigabit Ethernet (RJ45) for GUI/SNMP
ROUTE MMTP ALP	Up to 2x Gigabit Ethernet (RJ45)
GPS	1x PPS and 10 Mhz (BNC 50 Ω) 1x TNC for internal GPS - Option

OUTPUTS

STL stream	Up to 2x Gigabit Ethernet (RJ45)
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FEATURING

ATSC standard	V3.0 support
ATSC encapsulation	IP encapsulation into ALP packets ALP encapsulation into BB frames LMT tables generation
ATSC network configuration	In-band control of ATSC transmitters SubFrames management
SFN Adaptation	Integrated SFN adapter Timestamp generation
PLP management	Single PLP and Multi-PLP handling 4 PLP supported by default
STL output	Generation of STL stream over IP
IP management	4x Gigabit ports available Allow 1+1 redundant IP input and mirrored IP output
Monitoring and Supervision	Validation of ATSC parameters Full SNMP v2 support Easy-to-use WEB GUI



CHASSIS

Height	43 mm / 1.69 in.
Width	443,7 mm / 17.46 in.
Depth	322,8 mm / 12,70 in.
Format	1 RU, width 19"
Front Panel	LCD Display and controls
Power supply	100-240V 50/60Hz or 48V DC
Power consumption	20W/module

ORDERING CODES

HDc-Multi-220V High Density chassis with 220V input

HDc-Multi-48V High Density chassis with 48V input

Chassis Options

HDcMulti-In220VRedundant 110V/220V redundant power supply

HDcMulti-In48VRedundant 48V DC redundant power supply

HDm-ATSCcheduler ATSC Gateway module

Module Options

ATSCcheduler-MPLP8 Management of up to 8 PLP

